

REMARKS

In the Action dated February 23, 2004, the Examiner has sustained the rejection of claims 1-10 under 35 U.S.C. § 103(a) as being unpatentable over Schmidt et al., U.S. Patent No. 6, 101, 608 in view of Kamezawa, European Patent EP502744 A2. That rejection is once again respectfully traversed.

As set forth in the present specification at page 9, lines 14 et seq., the current application is directed to a technique for connecting a computer to a network whereby, upon receipt of a predetermined packet from the network, the computer system is awakened and a persistent display of that fact is provided so that a user may be able to recognize that execution of a so-called "Wake-up ON LAN" operation has been carried out or attempted.

In rejecting the present claims the Examiner relies upon Schmidt et al. for a teaching of a remote wake-up of a computer over a network, but notes that Schmidt et al. fails to show or suggest in any way the utilization of a dedicated display to indicate receipt of the predetermined wake-up packet.

In recognition to this substantial shortfall within the teachings of Schmidt et al., the Examiner cites Kamezawa for its teaching of an LCD display which the Examiner believes indicates the presence of a wake-up operation by an indication signal, citing column 3, lines 20-32.

In response to Applicant's arguments, the Examiner points out that *Kamezawa* teaches a display unit, noting LCD 11 in Figure 3 which displays when the signal indicates wake-up operation and thus, in the opinion of the Examiner, discloses displaying the receipt of a predetermined wake-up packet when delivered over the network as taught by *Schmidt*.

Applicant respectfully disagrees and points out to the Examiner that *Kamezawa* discloses a display control apparatus designed to increase the life expectancy of an LCD display by turning that LCD display on or off so that the display is not utilized when not necessary.

Thus, Applicant urges the Examiner to consider that any reasonable combination of Kamezawa with Schmidt et al. will result in a computer which is coupled to a local area network

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which may be the recipient of a "wake-up" signal over the local area network and which includes a display which may be placed in a hibernation state in response to a control signal generated by the local processor. Nothing within either reference provides the slightest suggestion for the utilization of a persistent display to indicate the receipt of a specified packet over a local area network, as set forth expressly within the claims of the present application and the only teaching within *Kamezawa* is, as noted above, the hibernation of the LCD display in response to a control signal generated by the local processing unit which displays output via that LCD display.

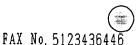
Further, Applicant respectfully urges the Examiner to consider that the claims of the present application expressly recite that the display occurs "responsive to receipt of a predetermined wake-up packet via said network..." and that nothing within Kamezawa shows or suggest the generation of a sleep or wake signal for the LCD display in response to receipt of a predetermined packet over the network. Further, the claims in the present application expressly recite that receipt of the predetermined wake-up packet is displayed "persistently" and further that the display occurs "utilizing a dedicated display, wherein said dedicated display is only utilized to indicate receipt of said predetermined wake-up packet."

Thus, Applicant urges the Examiner to consider that even if one having ordinary skill in the art would find the suggestion of Kamezawa that a local processor controls the status of its display device as suggestive of the claims of the present application which are expressly directed to providing an indication of the receipt of a predetermined packet over a network that the result combination fails to show or suggest the persistent display of the receipt of that packet utilizing a dedicated display as expressly set forth within the claims of the present application.

Consequently, Applicant therefore respectfully urges that claims 1-10 define patentable subject matter over this combination of references and withdrawal of the Examiner's rejection and passage of this application to issue is respectfully requested.

Claim 11 has been rejected by the Examiner under 35 U.S.C. § 103(a) as being unpatentable over *Kamezawa* as noted above in view of *Ichikawa*, U.S. Patent No. 4,783,654. That rejection is respectfully traversed.

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In what the Applicant believes to be a defective analysis, the Examiner applies Kamezawa to claim 11 and noting that the plurality of factors constitute a "sleep-in mode and a wake-up mode." While these indeed constitute two different modes of operation, the Examiner's attention is respectfully urged to claim 11 which expressly states that the computer changes from a power-save mode or a power-off mode to a normal operation mode "due to a plurality of factors..." and consequently, Applicant respectfully urges the Examiner to consider that a "sleep-in mode" cannot therefore constitute one of the plurality of factors which changes the computer from a power-save mode or power-off state to a normal operation mode as expressly set forth within this claim. Further, the citation by the Examiner of Ichikawa for its teaching of a reset signal fails to address this substantial deficiency within the primary reference relied upon by the Examiner and Applicant therefore urges that claim 11 defines patentable subject matter and allowance of this claim is also respectfully requested.

No extension of time for this response is believed to be necessary. However, in the event an extension of time is required, that extension of time is hereby requested. Please charge any fee associated with an extension of time as well as any other fee necessary to further the prosecution of this application to IBM CORPORATION DEPOSIT ACCOUNT No. 50-0563.

Respectfully submitted,

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